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AMENDMENT TRANSMITTAL LETTER				Docket No. M4065.0901/P901	
Application No. 10/699,652-Conf. #3940	Filing Date November 4, 2003	Examiner T. Nguyen	Art Unit 2827		
Applicant(s): Hagop A. Nazarian					
Invention: SERIAL TRANSISTOR-CELL ARRAY ARCHITECTURE					
TO THE COMMISSIONER FOR PATENTS					
Transmitted herewith is an amendment in the above-identified application.					
The fee has been calculated and is transmitted as shown below.					
CLAIMS AS AMENDED					
	Claims Remaining After Amendment	Highest Number Previously Paid	Number Extra Claims Present	Rate	
Total Claims	25	- 53 =		x	
Independent Claims	7	- 4 =	3	x 200.00	600.00
Multiple Dependent Claims (check if applicable) <input type="checkbox"/>					
Other fee (please specify):					
TOTAL ADDITIONAL FEE FOR THIS AMENDMENT:					600.00
<input checked="" type="checkbox"/> Large Entity <input type="checkbox"/> Small Entity					
<input type="checkbox"/> No additional fee is required for this amendment.					
<input type="checkbox"/> Please charge Deposit Account No. _____ in the amount of \$ _____. A duplicate copy of this sheet is enclosed.					
<input type="checkbox"/> A check in the amount of \$ _____ to cover the filing fee is enclosed.					
<input checked="" type="checkbox"/> Payment by credit card. Form PTO-2038 is attached.					
<input checked="" type="checkbox"/> The Director is hereby authorized to charge and credit Deposit Account No. <u>04-1073</u> as described below. A duplicate copy of this sheet is enclosed.					
<input checked="" type="checkbox"/> Credit any overpayment.					
<input checked="" type="checkbox"/> Charge any additional filing or application processing fees required under 37 CFR 1.16 and 1.17.					
 Gianni Minutoli Attorney Reg. No.: 41,198				Dated: <u>August 31, 2005</u>	
DICKSTEIN SHAPIRO MORIN & OSHINSKY LLP 2101 L Street NW Washington, DC 20037-1526 (202) 828-2232					



Docket No.: M4065.0901/P901
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Hagop A. Nazarian

Application No.: 10/699,652

Confirmation No.: 3940

Filed: November 4, 2003

Art Unit: 2827

For: SERIAL TRANSISTOR-CELL ARRAY
ARCHITECTURE

Examiner: T. Nguyen

AMENDMENT IN RESPONSE TO NON-FINAL OFFICE ACTION

MS Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

INTRODUCTORY COMMENTS

In response to the Office Action dated July 7, 2005 (Paper No. 20050701),
please amend the above-identified U.S. patent application as follows:

Amendments to the Claims are reflected in the listing of claims which begins
on page 2 of this paper.

Remarks begin on page 9 of this paper.

09/01/2005 JADD01 00000084 10699652

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